



**Memory Information and Procedures**

Product: 6680A, 6681A, 6682A, 6683A, 6684A, 6690A, 6691A, 6692A Power Supplies (includes all options unless otherwise noted)

Date: August 10, 2006

<b>Memory Type:</b> CMOS SRAM	<b>Memory Size:</b> 64K
<b>Memory Function:</b> Contains GPIB parameters	
<b>User Modifiable (Y/N):</b> No	<b>Volatile (Y/N):</b> Yes
<b>Memory Erase Processes:</b> Not user modifiable	

<b>Memory Type:</b> CMOS EEPROM	<b>Memory Size:</b> 2K
<b>Memory Function:</b> Contains GPIB address, serial & revision number, 10 storage locations, etc.	
<b>User Modifiable (Y/N):</b> Yes GPIB address and locations 0 thru 4	<b>Volatile (Y/N):</b> partial - locations 4 thru 9
<b>Memory Erase Processes:</b> Store zero in locations 0 thru 3 and set GPIB address to 5.	

<b>Memory Type:</b> CMOS ROM	<b>Memory Size:</b> 512K
<b>Memory Function:</b> Contains GPIB, SCPI and COMP language instructions	
<b>User Modifiable (Y/N):</b> No	<b>Volatile (Y/N):</b> No
<b>Memory Erase Processes:</b> Not user modifiable - OTP device	

<b>Memory Type:</b> CMOS ROM	<b>Memory Size:</b> 256K
<b>Memory Function:</b> Contains model specific instructions	
<b>User Modifiable (Y/N):</b> No	<b>Volatile (Y/N):</b> No
<b>Memory Erase Processes:</b> Not user modifiable - OTP device	

<b>Memory Type:</b> 2 each MPU w/o ROM	<b>Memory Size:</b> na
<b>Memory Function:</b> no memory	
<b>User Modifiable (Y/N):</b> No	<b>Volatile (Y/N):</b> na
<b>Memory Erase Processes:</b> Device has no memory	

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